

Amendment and Response

Applicant: Alessandro Minzoni, et al.

Serial No.: 10/574,236

Filed: January 18, 2007

Docket No.: 1438.111.101

Title: CLOCK RECEIVER CIRCUIT DEVICE, IN PARTICULAR FOR SEMI-CONDUCTOR COMPONENTS

REMARKS

The following remarks are made in response to the Non-Final Office Action mailed November 1, 2007. Claims 11-31 were rejected. With this Response, claims 11, 19, 24, 25, 30 and 31 have been amended and claims 20-23 have been canceled without prejudice or disclaimer of the subject matter contained therein. Claims 11-19 and 24-31 remain pending in the application and are presented for reconsideration and allowance.

Claim Rejections under 35 U.S.C. § 102

The Examiner rejected claims 11-31 under 35 U.S.C. § 102(b) as being anticipated by Keeth, U.S. Patent No. 5,852,378 (“Keeth”).

Independent claims 11 and 19 set forth a clock receiver circuit device comprising a receiver circuit converting the first clock signal and the second clock signal into respective inversely equal clock output signals having voltage levels different from voltage levels associated with the received first and second clock signals, independent claim 24 sets forth further connections of transfer gates are connected with each other and are jointly connected with a clock output for emitting a clock output signal having a voltage level different from voltage levels associated with received first and second clock signals, independent claim 25 sets forth a semi-conductor component having a receiver comprising a receiver circuit converting the first clock signal and the second clock signal into respective inversely equal clock output signals having voltage levels different from voltage levels associated with the received first and second clock signals, and independent claim 31 sets forth means for converting the first clock signal and the second clock signal into respective inversely equal clock output signals having voltage levels different from voltage levels associated with the received first and second clock signals.

Keeth teaches a single-ended to differential convertor for converting a single-end clock signal into a differential clock signal. Keeth does not teach a receiver circuit converting the first clock signal and the second clock signal into respective inversely equal clock output signals having voltage levels different from voltage levels associated with the received first and second clock signals, as set forth in independent claims 11, 24, 25 and 31. Nor does Keeth teach further connections of transfer gates are connected to apply a first voltage to a further connection of both

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a first transfer gate and a fourth transfer gate, and a second voltage, different from the first voltage, to a further connection of both a second transfer gate and a third transfer gate, as set forth in independent claims 19 and 30. For at least these reasons, the claims of the present application are patentably distinguishable from Keeth. Accordingly, withdrawal of the rejections is respectfully requested.

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CONCLUSION

In view of the above, Applicant respectfully submits that pending claims 11-19 and 24-31 are in form for allowance and are not taught or suggested by the cited reference. Therefore, reconsideration and withdrawal of the rejections and allowance of claims 11-19 and 24-31 are respectfully requested.

No fees are required under 37 C.F.R. 1.16(h)(i). However, if such fees are required, the Patent Office is hereby authorized to charge Deposit Account No. 50-0471.

The Examiner is invited to contact the Applicant's representative at the below-listed telephone numbers to facilitate prosecution of this application.

Any inquiry regarding this Amendment and Response should be directed to Steven E. Dicke at Telephone No. (612) 573-2002, Facsimile No. (612) 573-2005. In addition, all correspondence should continue to be directed to the following address:

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Respectfully submitted,

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